

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	10/708382	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/10/30 15:15
S2	14	(Ashton Gerry).in. or (Duncan Kevin A).in. or (Keim Terry D).in. or (Saitoh Toshiharu).in. or (Wilder Tad J).in.	US-PGPUB; USPAT; IBM_TDB	NEAR	ON	2006/10/30 16:17
S3	1	("6,304,122").PN.	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2006/10/31 10:33
S4	1	("5271019").PN.	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2006/10/31 10:36
S5	0	(10/896505).CCLS.	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2006/10/31 10:36
S6	1	10/896505	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/10/31 16:37
S8	3	(clock near (tree domain)) and ((phase or control) near delay) and ((scan near latch) or shift near register near latch)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/11/01 14:13
S9	610	((clock ADJ delay) (pulse ADJ generator)) with (("and" ADJ gate) and inverter)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/11/01 13:58
S10	15	(clock ADJ delay) with (("and" ADJ gate) and inverter)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/11/01 11:42
S11	12	"714"/\$.ccls. and S9	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/10/31 17:12
S12	195	S9 and (power ADJ supply)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/10/31 17:11
S13	3	"714"/\$.ccls. and S12	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/10/31 17:12
S14	6510	(charge ADJ pump) and (logic with (circuit or core))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/11/01 12:46

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S15	107	"714"/\$.ccls. and (charge ADJ pump) and (logic with (circuit or core))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/11/01 11:45
S16	99	S15 and (@pd<"20040227" or @ad<"20040227" or @prad<"20040227" or @rlad<"20040227")	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/11/01 12:52
S17	0	S16 and LSSD	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/11/01 11:49
S18	14	S16 and scan	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/11/01 11:49
S19	3065	S14 and (reduc\$3 or sav\$3 or low\$5 or minimiz\$5 preserv\$3 or maintain\$3 economiz\$3) near power	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/11/01 12:50
S20	2289	S19 and (power ADJ supply)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/11/01 12:51
S22	31	S19 and "714"/\$.ccls.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/11/01 12:52
S23	27	S22 and (@pd<"20040227" or @ad<"20040227" or @prad<"20040227" or @rlad<"20040227")	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/11/01 13:19
S24	1	"20040061539"	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/11/01 13:20
S25	15	(clock ADJ delay) with (("and" ADJ gate) and inverter)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/11/01 13:59
S26	1	("6,304,122").PN.	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2007/08/13 13:59
S27	16	(Ashton Gerry).in. or (Duncan Kevin A).in. or (Keim Terry D).in. or (Saitoh Toshiharu).in. or (Wilder Tad J).in.	US-PGPUB; USPAT; IBM_TDB	NEAR	ON	2007/09/14 12:49
S28	1	("6,304,122").PN.	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2007/09/14 12:49
S29	1	10/708382	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/09/14 12:49

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S30	4	((clock near (tree domain)) and ((phase or control) near delay) and ((scan near latch) or shift near register near latch)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/09/14 12:49
S31	1	10/896505	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/09/14 12:49
S32	1	("5271019").PN.	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2007/09/14 12:49
S33	0	(10/896505).CCLS.	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2007/09/14 12:49
S34	629	((clock ADJ delay) (pulse ADJ generator)) with (("and" ADJ gate) and inverter)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/09/14 12:49
S35	198	S34 and (power ADJ supply)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/09/14 12:49
S36	3	"714"/\$.ccls. and S35	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/09/14 12:49
S37	17	(clock ADJ delay) with (("and" ADJ gate) and inverter)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/09/14 12:49
S38	13	"714"/\$.ccls. and S34	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/09/14 12:49
S39	7389	(charge ADJ pump) and (logic with (circuit or core))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/09/14 12:49
S40	121	"714"/\$.ccls. and (charge ADJ pump) and (logic with (circuit or core))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/09/14 12:49
S41	109	S40 and (@pd<"20040227" or @ad<"20040227" or @prad<"20040227" or @rlad<"20040227")	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/09/14 12:49
S42	0	S41 and LSSD	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/09/14 12:49
S43	16	S41 and scan	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/09/14 12:49

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S44	3480	S39 and (reduc\$3 or sav\$3 or low\$5 or minimiz\$5 preserv\$3 or maintain\$3 economiz\$3) near power	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/09/14 12:49
S45	2580	S44 and (power ADJ supply)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/09/14 12:49
S46	17	(clock ADJ delay) with (("and" ADJ gate) and inverter)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/09/14 12:49
S47	34	S44 and "714"/\$.ccls.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/09/14 12:49
S48	29	S47 and (@pd<"20040227" or @ad<"20040227" or @prad<"20040227" or @rlad<"20040227")	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/09/14 12:49
S49	2	"20040061539"	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/09/14 12:49
S50	1	("6,304,122").PN.	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2007/09/14 12:49